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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10040608	FILING DATE 12/28/2001	CLASS 711	SUBCLASS 146	GAU 2188 2485	EXAMINER <i>Alkan 7.7.01</i>
**APPLICANTS: Orenstien Doron; Yuffe Marcelo;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		P10720			
Verified and Acknowledged Examiners's initials					
TITLE : Method and apparatus for communicating between integrated circuits in a low power mode <small>U.S. DEPT. OF COMM./PAT & TM-PTO-436L (Rev. 12-94)</small>					

Best Available Copy

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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